Docket No.

250848US2S

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF:

Osamu HIRABAYASHI

SERIAL NO:

New Application

GAU:

FILED:

Herewith

EXAMINER:

FOR:

SEMICONDUCTOR INTEGRATED CIRCUIT

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97

COMMISSIONER FOR PATENTS ALEXANDRIA, VIRGINIA 22313

SIR:

Applicant(s) wish to disclose the following information.

REFERENCES

- The applicant(s) wish to make of record the references listed on the attached form PTO-1449. Copies of the listed references are attached, where required, as are either statements of relevancy or any readily available English translations of pertinent portions of any non-English language references.
- ☐ A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

RELATED CASES

- Attached is a list of applicant's pending application(s) or issued patent(s) which may be related to the present application. A copy of the patent(s), together with a copy of the claims and drawings of the pending application(s) is attached along with PTO 1449.
- ☐ A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

CERTIFICATION

- ☐ Each item of information contained in this information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement.
- □ No item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the undersigned, having made reasonable inquiry, was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing of this statement.

DEPOSIT ACCOUNT

Please charge any additional fees for the papers being filed herewith and for which no check or credit card payment is enclosed herewith, or credit any overpayment to deposit account number <u>15-0030</u>. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND, MAIER & NEUSTADT, P.C.

Marvin J. Spivak

Registration No. 24,913

James D. Hamilton Registration No. 28,421

Customer Number

22850

Tel. (703) 413-3000 Fax. (703) 413-2220 (OSMMN 05/03) Docket No.

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LIST OF RELATED CASES

Docket Number	Serial or Patent Number	Filing or Issue Date	Inventor/ Applicant
237923US2S	10/443,068	05/22/03	HIRABAYASHI
246825US2S	10/739,123	12/19/03	HIRABAYASHI

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LIST OF REFERENCES CITED BY APPLICANT		Osamu HIRABAYASHI										
		FILING DATE		GROUP								
			Herewith									
U.S. PATENT DOCUMENTS												
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME CLASS		SUB FILING DATE						
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OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)												
C. L. CHEN, et al., "Error-Correcting Codes for Semiconductor Memory Applications: A State-of-the-Art Review",												
	AW	IBM J. RES. DEVELO	P, Vol. 28, No.	2, March 1984, pgs. 124-134				·				
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	AZ	Additional References sheet(s) attack					sheet(s) attached					
Examiner				Date Considered								
*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.												
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